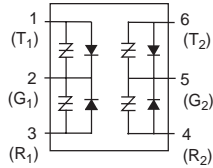


## Multiport SLIC Protector



This multiport line protector is designed as a single-package solution for protecting multiple twisted pair from overvoltage conditions. Based on a six-pin SOIC package, it is equivalent to four discrete DO-214AA packages. Available in surge current ratings up to 500 A for a 2x10  $\mu$ s event, the multiport line protector is ideal for densely populated line cards that cannot afford PCB inefficiencies or the use of series power resistors.

For specific design criteria, see details in Figure 3.33.

### Electrical Parameters

Part Number *	V <sub>DRM</sub> Volts	V <sub>S</sub> Volts	V <sub>T</sub> Volts	V <sub>F</sub> Volts	I <sub>DRM</sub> $\mu$ Amps	I <sub>S</sub> mAmps	I <sub>T</sub> Amps	I <sub>H</sub> mAmps	C <sub>O</sub> pF
	Pins 1-2, 2-3, 4-5, 5-6								
P0641U_	58	77	4	5	5	800	1	120	70
P0721U_	65	88	4	5	5	800	1	120	70
P0901U_	75	98	4	5	5	800	1	120	70
P1101U_	95	130	4	5	5	800	1	120	70
P1701U_	160	200	4	5	5	800	1	120	70

\* For individual "UA" and "UC" surge ratings, see table below.


#### General Notes:

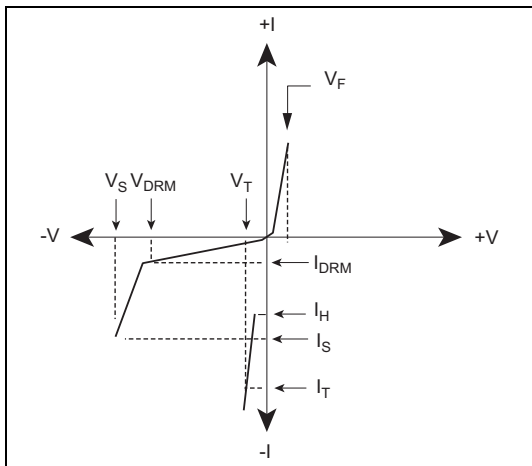
- All measurements are made at an ambient temperature of 25 °C. I<sub>PP</sub> applies to -40 °C through +85 °C temperature range.
- I<sub>PP</sub> is a repetitive surge rating and is guaranteed for the life of the product.
- V<sub>DRM</sub> is measured at I<sub>DRM</sub>.
- V<sub>S</sub> and V<sub>F</sub> are measured at 100 V/ $\mu$ s.
- Special voltage (V<sub>S</sub> and V<sub>DRM</sub>) and holding current (I<sub>H</sub>) requirements are available upon request.
- Off-state capacitance is measured across pins 1-2, 2-3, 4-5, or 5-6 at 1 MHz with a 2 V bias and is a typical value. Capacitance across pins 1-3 or 4-6 is approximately half. "UC" capacitance is approximately 2x the listed value for "UA" product.
- Parallel capacitive loads may affect electrical parameters.

### Surge Ratings

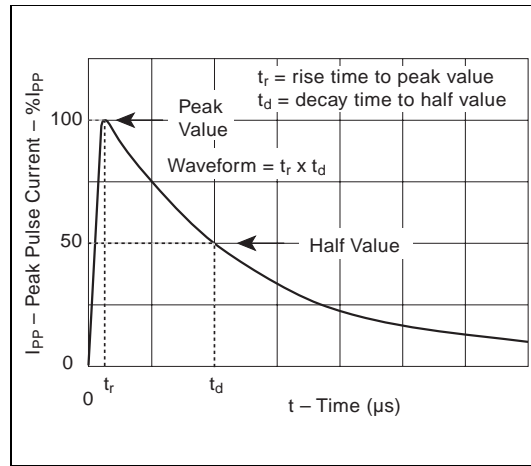
Series	I <sub>PP</sub> 2x10 $\mu$ s Amps	I <sub>PP</sub> 8x20 $\mu$ s Amps	I <sub>PP</sub> 10x160 $\mu$ s Amps	I <sub>PP</sub> 10x560 $\mu$ s Amps	I <sub>PP</sub> 10x1000 $\mu$ s Amps	I <sub>TSM</sub> 60 Hz Amps	di/dt Amps/ $\mu$ s
A	150	150	90	50	45	20	500
C	500	400	200	120	100	50	500

Thermal Considerations

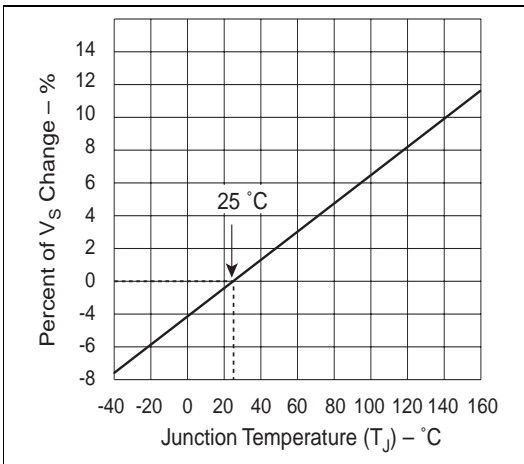
Package	Symbol	Parameter	Value	Unit
	$T_J$	Operating Junction Temperature Range	-40 to +150	$^{\circ}\text{C}$
	$T_S$	Storage Temperature Range	-65 to +150	$^{\circ}\text{C}$
	$R_{\theta JA}$	Thermal Resistance: Junction to Ambient	60	$^{\circ}\text{C}/\text{W}$



V-I Characteristics



$t_r \times t_d$  Pulse Wave-form



Normalized  $V_S$  Change versus Junction Temperature



Normalized DC Holding Current versus Case Temperature

Data Sheets